

FORM PTO-101 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. LT-155 CON	APPLICATION NO. 10/695,679
	APPLICANT Michael K. Mayes	
	FILING DATE October 28, 2003	GROUP 2819

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JBJ	4,943,807	07/1990	Early et al.	—	—	
	4,972,436	11/1990	Halim et al.	—	—	
	5,144,308	09/1992	Norsworthy	—	—	
	5,187,482	02/1993	Tiemann et al.	—	—	
	5,757,299	05/1998	Noro et al.	—	—	
	6,124,815	09/2000	Lee et al.	—	—	
	6,140,950	10/2000	Opreescu	—	—	
	6,169,506	01/2001	Opreescu et al.	—	—	
	6,208,279	03/2001	Opreescu	—	—	
	6,285,306	09/2001	Zrilic	—	—	
JBJ	6,337,645	01/2002	Pflaumer	—	—	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
EXAMINER INITIALS	
JBJ	M. P. Donadio, "CIC Filter Introduction," downloaded from the Internet at <a href="http://www.dspguru.com/info/tutor/cic.htm">http://www.dspguru.com/info/tutor/cic.htm</a> , Jul. 18, 2000.
JBJ	"Cascaded Integrator-Comb (CIC) Filter v. 1.0," Product Specification, LogiCore, Xilinx, Inc., Mar. 02, 2001.
JBJ	G. Noriega, "Sigma-Delta A/D Converters - Audio and Medium Bandwidths," Technical Notes - DT3, RMS Instruments, downloaded from the Internet at <a href="http://www.rmsinst.com/dt3.htm">http://www.rmsinst.com/dt3.htm</a> , Feb. 1996.

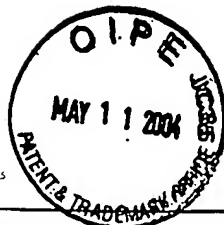
EXAMINER

*Glan Bruner Glanblau*

DATE CONSIDERED

*5/25/04*

EXAMINER: (Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. LT-155 CON	APPLICATION NO. 10/695,679
	APPLICANT Michael K. Mayes	
	FILING DATE October 28, 2003	GROUP 2819

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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JBSG	"An Overview of Data Converters," Application Note AN100, Philips Semiconductors, Dec. 1991.
	S. Park, "Principles of Sigma-Delta Modulation for Analog-to-Digital Converters," Communications Applications Manual, APR8, Motorola, DL411D/REV 1, 1993.
	E. Dijkstra, et al., "On the Use of Modulo Arithmetic Comb Filters in Sigma Delta Modulators," IEEE Proc. ICASSP, pp. 2001-2004, Apr. 1988.
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	J. C. Candy, "Decimation for Sigma Delta Modulation," IEEE Transactions on Communications, vol. COM-34, pp. 72-76, Jan. 1986.
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	"ADC0801/ADC0802/ADC0803/ADC0804/ADC0805: 8-Bit $\mu$ P Compatible A/D Converters, datasheet, National Semiconductor, Nov. 1999.
	"AD650: Voltage-to-Frequency and Frequency-to-Voltage Converter," datasheet, Analog Devices, 2000.
	"LM231A/LM231/LM331A/LM331: Precision Voltage-to-Frequency Converters," datasheet, National Semiconductor, Jun. 1999.
	"ALD500AU/ALD500A/ALD500: Precision Integrating Analog Processor," datasheet, Advanced Linear Devices, Inc., 1999.
	"AD1170: High Resolution, Programmable Integrating A/D Converter," datasheet, Rev. A, Analog Devices, Aug. 1999.
	"LTC2400: 24-Bit $\mu$ Power No Latency $\Delta\Sigma^{\text{TM}}$ ADC in SO-8," datasheet, Linear Technology, 1998.
JBSG	"LTC2410: 24-Bit No Latency $\Delta\Sigma^{\text{TM}}$ ADC with Differential Input and Differential Reference," datasheet, Linear Technology, 2000.

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*Jean Bruner Jonsland*

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